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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application Serial No	10/624,628
Filing Date	July 21, 2003
Inventor	Luan C. Tran
Assignee	Micron Technology, Inc.
	2812
Examiner	Kennedy, Jennifer M.
	Ml22-2356
Title:	Methods of Forming Semiconductor Constructions

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1276 OG 55). No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated:

Bv:

Jennifer J. Taylor

Reg. No. 48,711

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EXAMINER			DATE CON	DATE CONSIDERED								
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.												

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